

## Department of Electronics & Communication Engineering National Institute of Technology Silchar Silchar - INDIA

#### Advertisement No: NITS/Dean (RC)/ECE/640/04/2024

Applications are invited from Indian citizens for a temporary post of **Project Associate-I** in the Department of Electronics & Communication Engineering, NIT Silchar, Assam, for the following project.

Date: 02.09.2024

	Design and Development of Low Power Low Latency Non-Invasive Seizer Detector System.
Sponsored Agency	Ministry of electronics & Information Technology (MeitY), Government of India
Project sanction number	EE-9/2/2021-R&D-EDated 22 May 2023
No. of posts	01(one), Project Associate-I (PA-I)
Duration	1 years and extendable till the completion of the project (10/02/2027)
Principal Investigator(P.I & Co-PI)	Prof. K.L. Baishnab & Dr. Koushik Guha

General Information and Terms & Conditions

1	Ago Limit	Not more than 35 Years. SC/ST/OBC/Women/PwD candidates will get			
1	Age Limit				
		the age relaxation as per DST, Govt. of India norms.			
2	Essential Qualification	BE/B.Tech with valid GATE Score or M.E/M.Tech in Microelectronics &			
	and experiences	VLSI Design with good academic record from a recognized			
		University/Institute.			
3	Desirable Experience	Knowledge of working in Linux environment, Knowledge of FPGA,			
	for both the posts	Knowledge of RTL Coding in Verilog, Knowledge of FPGA Implementation,			
		Knowledge of bio-logical background related to the project work.			
4	Salary	• Rs.31000/- per month with valid Gate score and Rs 25000/month without			
		valid Gate score for M. Tech candidates.			
		HRA along with other allowances/increment will be provided as per			
		project/ institute Norms.			
		• Rs.25000/- per month with valid Gate score for B. Tech candidates.			
		HRA along with other allowances/increment will be provided as per			
		project/ institute Norms.			
		Candidates may also get an opportunity to pursue M.Tech/PhD as per			
		Institute Norms.			
5.	Last date of receiving/	16 <sup>th</sup> September, 2024			
	Submission of filled				
	up application form				
6		Applicants are requested to submit the signed scanned copy of their filled-			
6	up application form	in application form along with CV and the all supporting documents to			
6	up application form	in application form along with CV and the all supporting documents to the P.I. through email at <a href="mailto:klb@ece.nits.ac.in">klb@ece.nits.ac.in</a> and <a href="mailto:koushik@ece.nits.ac.in">koushik@ece.nits.ac.in</a>			
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Sd/-

Associate Dean(R&C)

NIT Silchar.



# National Institute of Technology, Silchar

# FORMAT of APPLICATION For Project Associate-I

Paste recent passport sizecolored photograph

1.	Advertise	ement no				. dated:
2.	Title of	the Project				
3.	. Name of	the post applied	for		· • • • • • • • • • • • • • • • • • • •	
4. ]	Departmei	nt:				
5. ]	Name of t	he applicant (in	block lette	rs):		
6.	Father's/	'Husband's Nam	ne:			
7.	Date of B	Sirth (DD-MM-YY	YYY):			
8.	Age as on	( 16.09.2024)				
9.	Postal Ac	idress:				
E-r	mail:	Ph	none no.:			
		nt Address:				
_	1					
11.	. Nationali	ıty:				
12.	. Marital St	atus:				
13.	. Whet her	belongs to SC/ST	Г/РН/ОВС	ር:(If yes, a	ttach ce	rtificate):
14.	, Educatio:	nal Qualification	n:(Starting	from Clas	ss X)	
		Board/University	Divis ion	of.	1	Subjects
Pas	ssed		1011	Marks/ CGPA	Passin g	
				<del></del>	-	

15. NET/GATE Examinations Passed:				
a.	NET	II.		GATE

Subject	Qualifying date	Valid up to	Subject	Score	Valid up to

16. Details of employments:

	Name of The post	Period	Scale of pay and present pay	Nature of duty

17. Any other relevant information that you may like to furnish:

## Declaration

	I declared at the above information is true and correct to	the best of my knowledge and
belief	: •	
Date	2:	
Place	e	Signature of the applicant