

Department of Electronics & Communication Engineering National Institute of Technology Silchar Silchar – INDIA

Date: 05-10-2023

Advertisement No.: NITS/Dean (RC)/ECE/640/01/2023

Applications are invited from Indian citizens for a temporary post of **Project Associate-I and Project Associate-II** in the Department of Electronics & Communication Engineering, NIT Silchar, Assam, for the following project.

Title of the project	Design and Development of Low Power Low Latency Non-Invasive Seizer Detector System.
Sponsored Agency	Ministry of electronics & Information Technology (MeitY), Government of India
Project sanction number	EE-9/2/2021-R&D-EDated 22 May 2023
No. of posts	02(Two), Project Associate-I (PA-I) and Project Associate-II (PA-II)
Duration	1 years and extendable till the completion of the project (10/02/2027)
Principal Investigator (P.I & Co-PI)	Prof. K.L. Baishnab & Dr. Koushik Guha

General Information and Terms & Conditions

1	Essential Qualification	For PA-I
	and experiences	M.E/M. Tech in Microelectronics & VLSI Design with minimum 8.0 CGPA with good academic record from a recognized University/Institute. • For PA-II M.E/M. Tech in Microelectronics & VLSI Design with minimum 8.0 CGPA with good academic record from a recognized University/Institute. Experience for PA-II: Minimum 5 Years experience in Chip Design and Tape-out with fullknowledge of VLSI CAD Tools Installation and Complete Lab Setup.
2	Desirable Experience for both the posts	Knowledge of working in Linux environment, Knowledge of FPGA, Knowledge of RTL Coding in Verilog, Knowledge of FPGA Implementation, Knowledge of bio-logical background related to the project work.
3	Salary	 For PA-I, Rs.31000/- per month. For PA-II, Rs.54000/- per month. HRA along with other allowances/increment will be provided as per project/ institute Norms. Candidates may also get an opportunity to pursue PhD as per Institute Norms.
4.	Last date of receiving/ Submission of filled up application form	20 th October 2023
5	Reporting	Applicants are requested to submit the signed scanned copy of their filled- in application form along with CV and the all supporting documents to the P.I. through email at klb@ece.nits.ac.in and koushik@ece.nits.ac.in with subject line" Application for the post of Project Associate-II Project Associate-II underMeitYprojectEE- 9/2/2021-R&D-E".Call letter for shortlisted candidates will be sent through email.
6	Date of interview, time, and venue	Interview date and mode (online/offline) will be notified in due course of time through email.
7	No objection certificate	Employed candidates are to submit no objection certificate from his/her Employer at the time of interview.
8	Instituteright	The Institute has the discretion to restrict the number of candidates to be called for the interview on the basis of screening. No candidate may be recruited if suitable candidates are not found.
9	Signing of the agreement	Selected candidates shall have to sign an agreement at the time of joining.



National Institute of Technology, Silchar

FORMATOFAPPLICATION

For Project Associate-I/Project Associate-II

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1. Advertisement no					
2. Title of the Project-					
3. Name of the post applied for					
4. Department:					
5. Name of the applicant (in block letters):					
6. Father's/Husband'sName:					
7. Date of Birth(DD-MM-YYYY):					
8. Postal Address:					
E-mail: Phoneno.:					
9. PermanentAddress:					
10. Nationality:					
11. MaritalStatus:					
12. Whether be longs to SC/ST/PH/OBC:(If yes, attach certificate):					

Exam Passed	Board/University	1	Divis ion	of	Year of Passing	Subjects

13. Educational Qualification: (Starting from Class X)

1 i. Details of elliployillelits.	14.	Details	of emp	loyments:
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Sl. No.	Name of theOrganization	Nameofth post	Perio	Scale of payandpres nt	Natureofduty

15. Any other relevant information that you may like to furnish:

Declaration

I declare that the above information belief.	are true and correct to the best of my knowledge and
Date:	
Place	Signature of the applicant